

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

Applicants: JUNG et al.)	For: "Methods of Manufacturing a
)	Semiconductor Device"
Serial No.: To be assigned)	
)	Group Art Unit: To be determined
Filed: November 25, 2003)	
)	Examiner: To be determined

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

This information disclosure statement is being filed with the application. Therefore, in accordance with 37 CFR §1.97(b), no certification or fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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Respectfully submitted,

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November 25, 2003

Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. 20061/OF03P197	Serial No. To be assigned
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		Applicant JUNG et al.	
		Filing Date 11/25/03	Group Art Unit To be determined

U.S. PATENT DOCUMENTS							
*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,255,217	7/3/01	Agnello et al.	438	687	
	A02	6,211,085	4/3/01	Liu	438	687	
	A03						

FOREIGN PATENT DOCUMENTS							
*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes No
	B01						
	B02						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
	C01	
	C02	

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.</p>	